

Appl. No. 10/604,646
Amdt. dated January 24, 2006
Reply to Office action of November 01, 2005

Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of Claims:

- 5 Claim 1 (currently amended) A polysilicon thin film transistor liquid crystal display comprising:
- a panel;
 - a common voltage layer formed in the panel;
 - a plurality of display cells;
 - 10 a plurality of scan lines formed in the panel and coupled to the display cells;
 - a plurality of data lines formed in the panel and coupled to the display cells; and
 - a plurality of common voltage drivers formed in the panel, the common
 - 15 voltage drivers comprising polysilicon thin film transistors, and
 - each common voltage driver being for generating a common voltage applied to the common voltage layer.

Claim 2 (cancelled)

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Claim 3 (original) The polysilicon thin film transistor liquid crystal display of claim 1 wherein the common voltage is an alternating voltage.

Claim 4 (original) The polysilicon thin film transistor liquid crystal display of claim 1 further comprising:

- a scan line driver coupled to the plurality of scan lines;
- at least a data line driver coupled to the plurality of data lines; and

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a timing control circuit for generating a timing signal;
wherein the scan line driver and the data line driver control operations
of the display cells based on the timing signal.

5 Claim 5 (cancelled)

Claim 6 (original) The polysilicon thin film transistor liquid crystal
display of claim 1 further comprising an interface for receiving and
transmitting an image signal such that the display cells operate based on
10 the image signal.

Claim 7 (original) The polysilicon thin film transistor liquid crystal
display of claim 1 wherein each display cell further comprises:

a liquid crystal component comprising:
15 a pixel electrode; and
a common electrode coupled to the common voltage layer; and
a polysilicon thin film transistor comprising:
a gate electrically connected to a corresponding scan line;
a source electrically connected to a corresponding data line;
20 and
a drain electrically connected to the pixel electrode of the
liquid crystal component.